

# MUHAMMAD MOHSIN

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## OBJECTIVE:

Seeking to apply my machine learning and data analysis concepts as a summer intern. I studied and implemented some machine learning algorithms on a personal interest and the experience from this internship will aid me in becoming better at this field.

## EXPERIENCE

### Backend Developer Intern

2 months python Django developer (summer 2019)

Projects:

Payment Gateway Integration

CRUD Blog and Gallery Site

User Authentication

## COURSES

### Coursera (still continuing)

Machine Learning (Andrew Ng.) -- Stanford University

Course audited

### Edx (still continuing)

Python For Data Science

UCSanDiegoX – DSE200x

## EDUCATION

### B.SC (ELECTRICAL ENGINEERING 2017-2021)

University of Engineering and Technology

CGPA: **3.861/4.0** (4 semesters) Percentage:**97.600**

### F.SC (PRE-ENGINEERING 2015-2017)

Nishat College of Science, Lodhi Colony, Multan.

Marks: **1036/1100**

Percentage:**94.1818**

### Achievements:

Gold Medalist\First Position in Multan Board Pre-Engineering Group.

### MATRICULATION (SCIENCE GROUP 2014-2015)

Nishat High School, Pir Khurshid Colony, Multan.

Marks: **1053/1100**

Percentage: **95.7272**

## **SKILLS: TECHNICAL**

- Machine learning (Octave/Matlab)
  - Logistic Regression
  - Linear Regression
  - K means
  - Recommender Systems
- Python
  - Numpy
  - Pandas Dataframes
  - Django
- Micro-Controller Programming
- C Programming
- MATLAB\OCTAVE
- Circuit Simulators
- Industrial Softwares
  - Allen Bradley RS logix 5000\500\5
  - GE Proficy Machine Edition

## **SOFT**

- Project Management
- Communication Skills
- Team Management
- Adaptability
- Work Ethic

## **PROJECTS:**

- Self Balancing Robot with Tiva TM4C1233H6PM
- House price prediction using linear regression in MATLAB
- ATMEGA 128 Arduino Development Board PCB.
- Bluetooth Controlled Car
- Automatic Lightning System using Light Dependent Resistance
- Library Management System (C programming)
- Contact Book (C programming)
- AC to DC Power Supply Design and PCB
- Stop Watch with Verilog HDL on FPGA Spartan 3e starter Board